

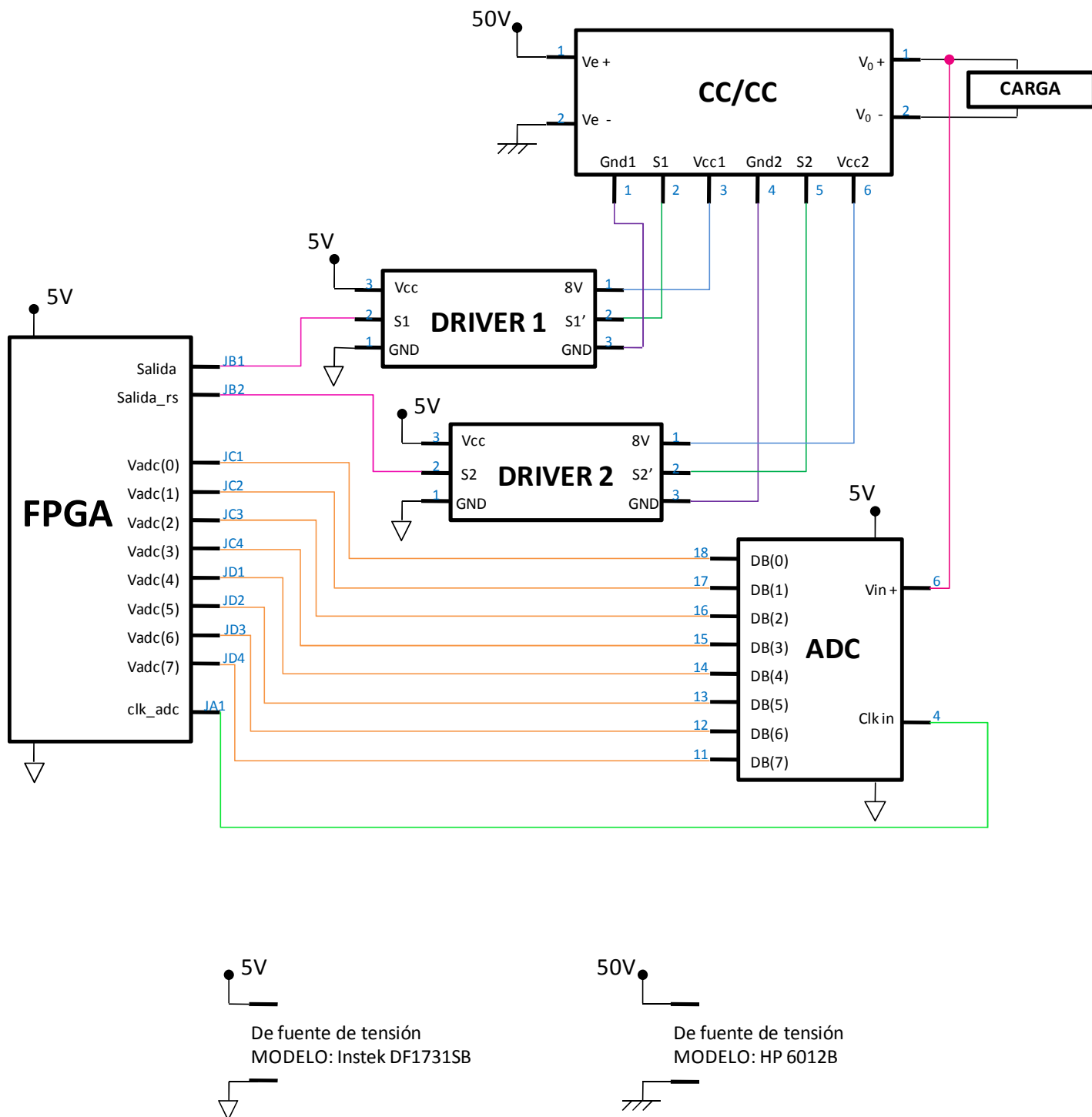
# PLANOS

# Planos

La siguiente tabla muestra la relación de los planos que se adjuntan en este apartado:

| NÚMERO DE PLANO | DESCRIPCIÓN                                              |
|-----------------|----------------------------------------------------------|
| 1               | Esquema eléctrico del sistema completo                   |
| 2               | Esquema eléctrico del convertidor                        |
| 3               | Fotolito de la cara inferior de la placa del convertidor |
| 4               | Ubicación de los componentes de la placa del convertidor |
| 5               | Esquema eléctrico del driver                             |
| 6               | Fotolito de la cara superior de la placa del driver      |
| 7               | Fotolito de la cara inferior de la placa del driver      |
| 8               | Ubicación de los componentes de la placa del driver      |
| 9               | Esquema de la conexión del ADC                           |

**Tabla 1. Índice de planos.**



Title

ESQUEMA ELÉCTRICO DEL SISTEMA COMPLETO

Size

A4

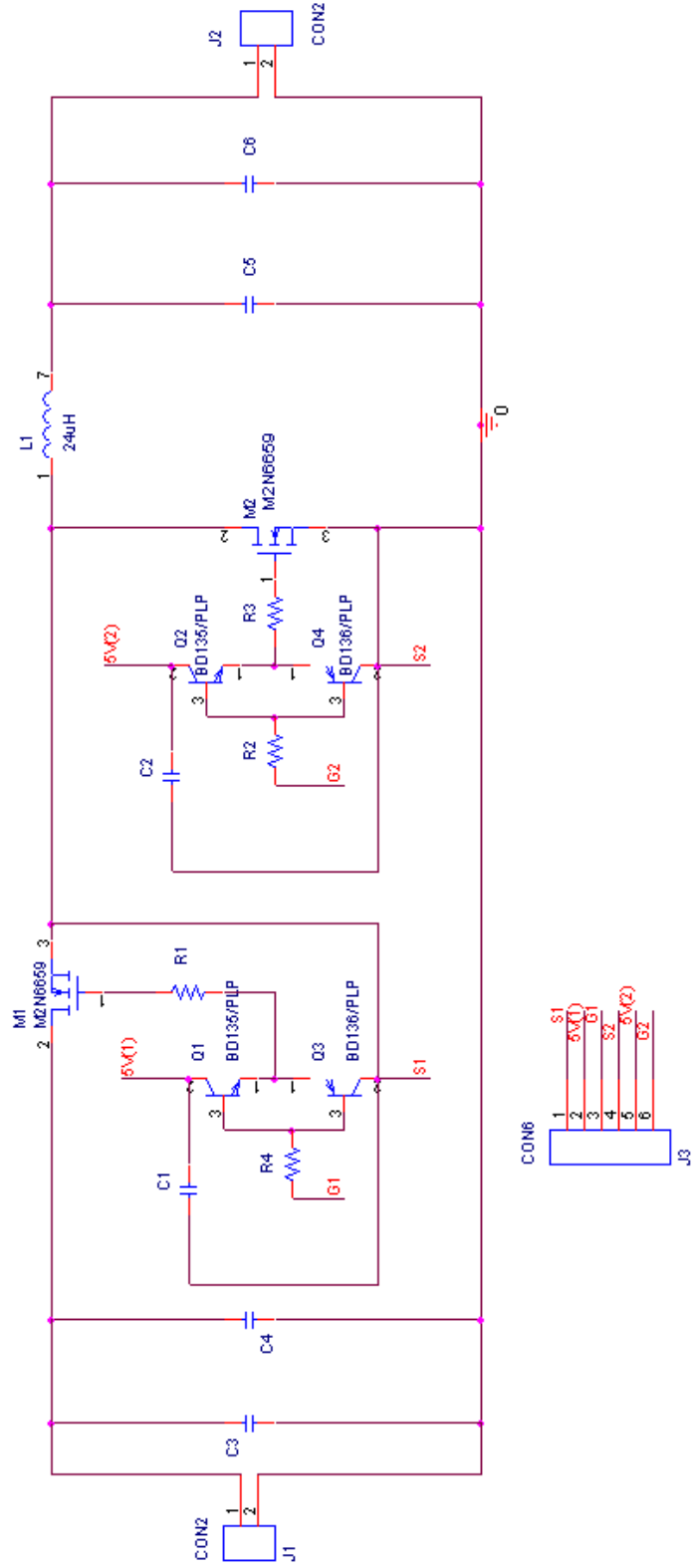
Document number

PLANO N° 1

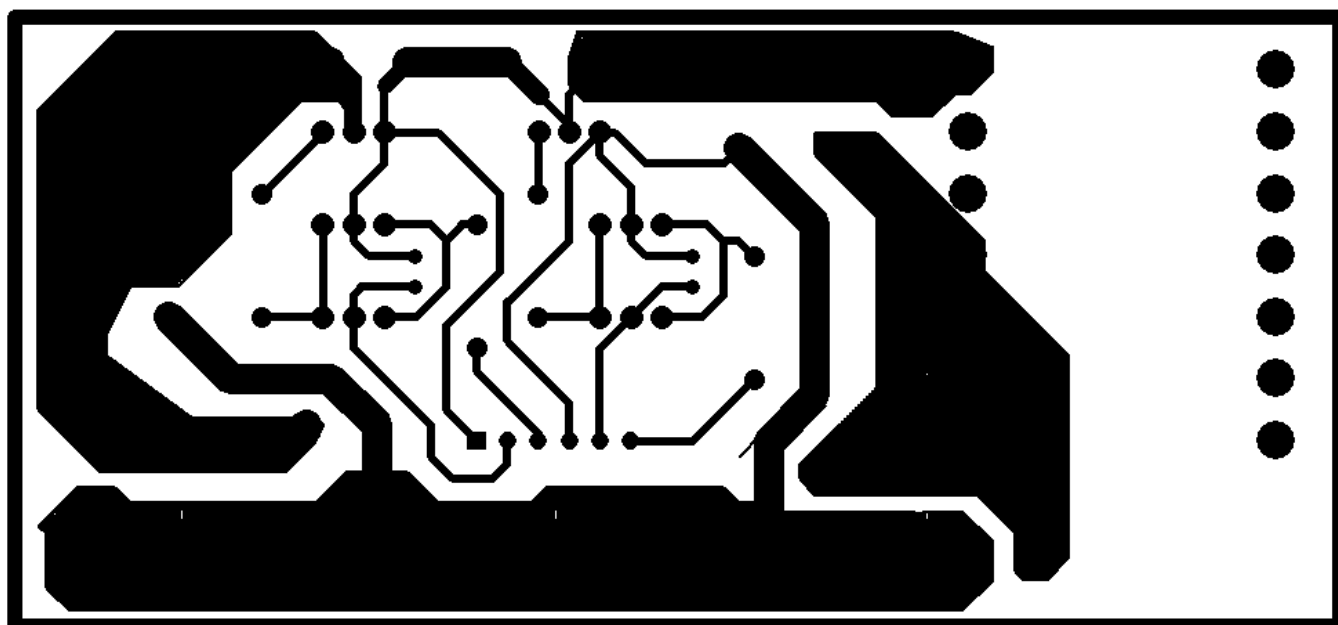
Rev

Date: Thursday, November 29, 2012

Sheet 1 of 1



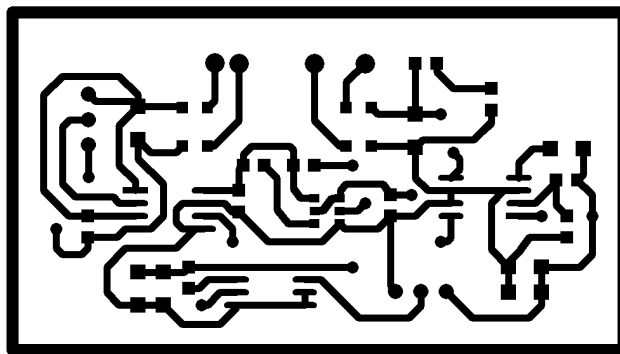
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|-----------------------------------|-----------------|-----|
| Title                             |                 |     |
| ESQUEMA ELÉCTRICO DEL CONVERTIDOR |                 |     |
| Size                              | Document number | Rev |
| A4                                | PLANO N° 2      |     |
| Date: Thursday, November 29, 2012 |                 |     |
| Sheet 1 of 1                      |                 |     |



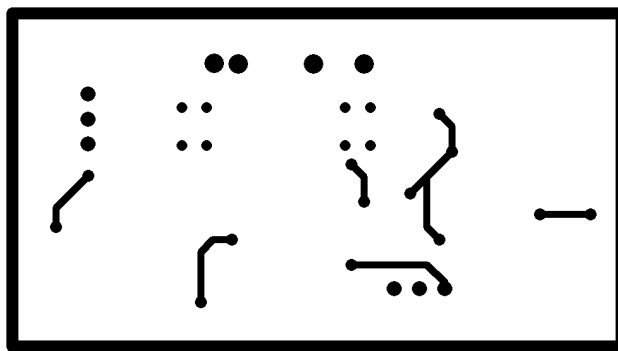
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|----------------------------------------------------|-----------------------------|--------------|
| Title                                              |                             |              |
| FOTOLITO CARA INFERIOR DE LA PLACA DEL CONVERTIDOR |                             |              |
| Size                                               | Document number             | Rev          |
| A4                                                 | PLANO N° 3                  |              |
| Date:                                              | Thursday, November 29, 2012 | Sheet 1 of 1 |



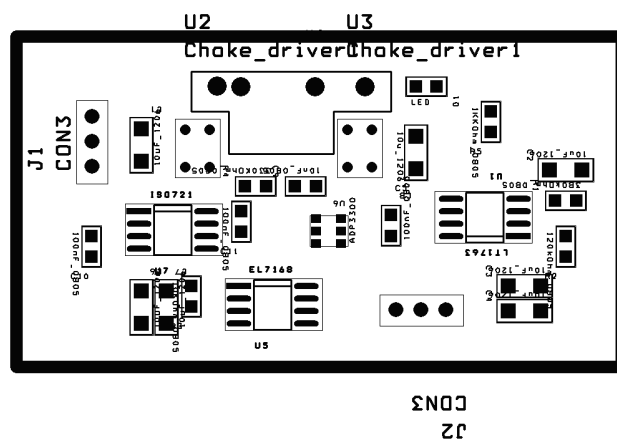




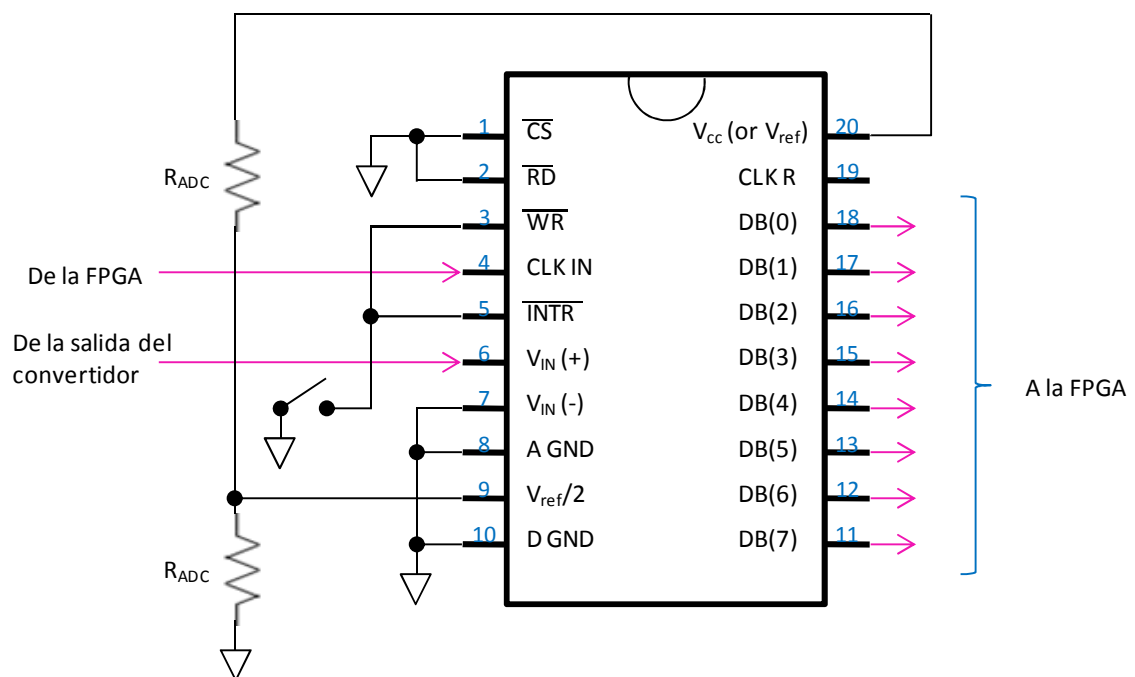
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|-----------------------------------------------|-------------------------------|--------------|
| Title                                         |                               |              |
| FOTOLITO CARA SUPERIOR DE LA PLACA DEL DRIVER |                               |              |
| Size<br>A4                                    | Document number<br>PLANO N° 6 | Rev          |
| Date:                                         | Thursday, November 29, 2012   | Sheet 1 of 1 |



|                                               |                             |              |
|-----------------------------------------------|-----------------------------|--------------|
| Title                                         |                             |              |
| FOTOLITO CARA INFERIOR DE LA PLACA DEL DRIVER |                             |              |
| Size                                          | Document number             | Rev          |
| A4                                            | PLANO N° 7                  |              |
| Date:                                         | Thursday, November 29, 2012 | Sheet 1 of 1 |



|                                                     |                             |              |
|-----------------------------------------------------|-----------------------------|--------------|
| Title                                               |                             |              |
| UBICACIÓN DE LOS COMPONENTES DE LA PLACA DEL DRIVER |                             |              |
| Size                                                | Document number             | Rev          |
| A4                                                  | PLANO N° 8                  |              |
| Date:                                               | Thursday, November 29, 2012 | Sheet 1 of 1 |



$$R_{ADC} = 12K\Omega$$

|                                   |                               |     |
|-----------------------------------|-------------------------------|-----|
| Title                             |                               |     |
| ESQUEMA DE CONEXIÓN DEL ADC       |                               |     |
| Size<br>A4                        | Document number<br>PLANO N° 9 | Rev |
| Date: Thursday, November 29, 2012 | Sheet 1 of 1                  |     |

# Bill of materials

- **Convertidor CC/CC**

| Nº | UNIDADES | NOMBRE DEL COMPONENTE | REFERENCIA | VALOR | FOOTPRINT          |
|----|----------|-----------------------|------------|-------|--------------------|
| 1  | 2        | Cerámico              | C1         | 100n  | RAD/.200X.100/.031 |
| 2  |          |                       | C2         | 100n  |                    |
| 3  | 1        | Electrolítico         | C3         | 470u  | CP_R7.5_D17        |
| 4  | 1        |                       | C6         | 1000u |                    |
| 5  | 1        | MKT                   | C4         | 10u   | CPLAS_C2           |
| 6  | 1        |                       | C5         | 4.7u  | CPLAS_C3           |
| 7  | 2        | CON2                  | J1         | -     | REGLETA2           |
| 8  |          |                       | J2         | -     |                    |
| 9  | 1        | CON6                  | J3         | -     | CONEC_FILA_6       |
| 10 |          | E30                   | L1         | 24u   | E30_14             |
| 11 | 2        | IRF540                | M1         | -     | TO220              |
| 12 |          |                       | M2         | -     |                    |
| 13 | 2        | BD137                 | Q1         | -     | TO126              |
| 14 |          |                       | Q2         | -     |                    |
| 15 | 2        | BD138                 | Q3         | -     |                    |
| 16 |          |                       | Q4         | -     |                    |
| 17 | 1        | Resistencia 1/4W      | R1         | 1.8   | RESIS_1/4          |
| 18 | 1        |                       | R3         |       |                    |
| 19 | 1        |                       | R2         | 56    |                    |
| 20 | 1        |                       | R4         |       |                    |

Para las referencias, ver Plano N°2: Esquema eléctrico del convertidor.

• Drivers

| Nº | UNIDADES | NOMBRE DEL COMPONENTE | REFERENCIA | VALOR | FOOTPRINT       |
|----|----------|-----------------------|------------|-------|-----------------|
| 1  | 7        | Condensador SMD 1206  | C1         | 10u   | SM/C_1206_SMT   |
| 2  |          |                       | C2         |       |                 |
| 3  |          |                       | C3         |       |                 |
| 4  |          |                       | C4         |       |                 |
| 5  |          |                       | C5         |       |                 |
| 6  |          |                       | C6         |       |                 |
| 7  |          |                       | C7         |       |                 |
| 8  | 3        | Condensador SMD 0805  | C8         | 100n  | SM/C_0805_SMT   |
| 9  |          |                       | C10        |       |                 |
| 10 |          |                       | C11        |       |                 |
| 11 | 1        |                       | C9         | 10n   |                 |
| 12 | 1        | LED                   | D1         | -     |                 |
| 13 | 2        | CON3                  | J1         | -     | CON3DRIVER      |
| 14 |          |                       | J2         |       |                 |
| 15 | 1        | Resistencia SMD 0805  | R1         | 380k  | SM/C_0805_SMT   |
| 16 | 1        |                       | R2         | 120k  |                 |
| 17 | 1        |                       | R3         | 10k   |                 |
| 18 | 1        |                       | R4         | 330k  |                 |
| 19 | 1        |                       | R5         | 1k    |                 |
| 20 | 1        | LT1763                | U1         | -     | SOIC_8D_SMT     |
| 21 | 2        | Choke driver          | U2         | -     | EMI_CHOKE_DLW   |
| 22 |          |                       | U3         |       |                 |
| 23 | 1        | DHC010512SN7          | U4         | -     | DHC010512       |
| 24 | 1        | EL7156                | U5         | -     | SOIC_8D_SMT     |
| 25 | 1        | ADP3300               | U6         | -     | SM/SOT23_AD3300 |
| 26 | 1        | ISO721                | U7         | -     | SOIC_8D_SMT     |

Para las referencias, ver Plano N°5: Esquema eléctrico del driver.